

2811

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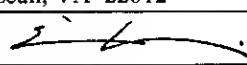
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| <b>TRANSMITTAL FORM</b><br><i>(to be used for all correspondence after initial filing)</i> | Application Number     | 09/433,705       |
|  | Filing Date            | November 4, 1999 |
|  | First Named Inventor   | Shunpei YAMAZAKI |
|  | Group Art Unit         | 2811             |
|  | Examiner Name          | S. Loke          |
|  | Attorney Docket Number | 0756-2062        |
| Total Number of Pages in This Submission   |                        |                  |

**ENCLOSURES (check all that apply)**

|   |   |   |
|---|---|---|
| <input type="checkbox"/> Fee Transmittal Form<br><input type="checkbox"/> Fee Attached<br><input checked="" type="checkbox"/> Amendment / Reply<br><input type="checkbox"/> After Final<br><input type="checkbox"/> Affidavits/declaration(s)<br><input type="checkbox"/> Extension of Time Request<br><input type="checkbox"/> Express Abandonment Request<br><input checked="" type="checkbox"/> Information Disclosure Statement and Notification of Related Application<br><input type="checkbox"/> Certified Copy of Priority Document(s)<br><input type="checkbox"/> Response to Missing Parts/Incomplete Application<br><input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53 | <input type="checkbox"/> Assignment Papers (for an Application)<br><input type="checkbox"/> Drawing(s)<br><input type="checkbox"/> Licensing-related Papers<br><input type="checkbox"/> Petition<br><input type="checkbox"/> Petition to Convert to a Provisional Application<br><input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address<br><input type="checkbox"/> Terminal Disclaimer<br><input type="checkbox"/> Request for Refund<br><input type="checkbox"/> CD, Number of CD(s) | <input type="checkbox"/> After Allowance Communication to Group<br><input type="checkbox"/> Other |
| Remarks<br><input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees required or credit any overpayments to Deposit Account No. 19-2380 for the above identified docket number.   |   |   |

**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

|                         |  |
|-------------------------|--|
| Firm or Individual name | Eric J. Robinson, Reg. No. 38,285<br>Nixon Peabody LLP<br>8180 Greensboro Drive<br>Suite 800<br>McLean, VA 22012 |
| Signature               |                               |
| Date                    | October 15, 2001   |

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )  
Shunpei YAMAZAKI ) Group Art Unit: 2811  
Serial No. 09/433,705 ) Examiner: S. Loke  
Filed: November 4, 1999 )  
For: METHOD OF FABRICATING )  
A SEMICONDUCTOR DEVICE )

#15/B  
11-13-01  
Payton

**AMENDMENT AND RESPONSE TO NON-COMPLIANT AMENDMENT**

Honorable Commissioner for Patents

Washington, D.C. 20231

Sir:

In response to the *Notice of Non-Compliant Amendment* of September 13, 2001, please amend the subject application as follows:

**IN THE CLAIMS:**

Sub C<sup>2</sup> Please amend claim 13.

13. (Amended) A semiconductor device comprising:  
a first thin film transistor comprising:  
a semiconductor island on an insulating surface;  
a channel region in the semiconductor island;  
at least an LDD region being in contact with the channel region and including a first impurity region and a second impurity region, said first impurity region being in contact with the channel region and said second impurity region being in contact with the first impurity region;  
at least a third impurity region being in contact with the second impurity region; a gate electrode being formed over the semiconductor island with a gate insulating film interposed therebetween and having a first gate electrode and a second electrode being formed on the first gate electrode,  
wherein the first gate electrode has at least a taper portion and a flat portion,  
wherein the first impurity region is overlapped with the taper portion of the first gate electrode with the gate insulating film interposed therebetween,